

ABSTRACT OF THE DISCLOSURE

An improved memory device is operable in a plurality of modes. The improved memory device has an interface logic decoding circuit which decodes the signals on the communication bus with the external integrated circuit. Depending upon the decoded signals from the communication bus, the memory device is operable in one of the plurality of protocol modes. In particular, one of the applications of the improved memory device is to operate in either the LPC or the FWH communication protocol.